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The screenshot displays the Quartus 14.1 IDE interface. The main window shows the 'Design Overview' for a project named 'MODEM_YKB_KART_TEST'. The 'Project Summary' pane on the right provides details about the project file, module name, target device, and product version. Below this, the 'Parser Errors' and 'Warnings' sections are visible, indicating 4 warnings. The 'Utilization Summary' table shows resource usage for various components. A 'Detailed Reports' section is also present, showing 0 errors and 4 warnings. In the foreground, a 'Select IP' dialog box is open, displaying a list of IP cores with columns for Name, Version, AXI4, AXI4-Stream, AXI4-Lite, Status, License, Vendor, and Library. The dialog box is titled 'Create Coregen or Architecture Wizard IP Core' and includes a search bar and checkboxes for IP version compatibility.

MODEM_YKB_KART_TEST Project Status			
Project File:	FTO_MODEM_YKB.xise	Parser Errors:	No Errors
Module Name:	MODEM_YKB_KART_TEST	Implementation State:	Synthesized
Target Device:	xc7k355t-1ffg901	Errors:	No Errors
Product Version:	ISE 14.6	Warnings:	4 Warnings (4 new)
		Routing Results:	
		Timing Constraints:	
		Final Timing Score:	

Utilization Summary (estimated values)			
Available	Utilization		
25	445200	0%	
24	222600	0%	
24	25	96%	
27	300	9%	
5	32	15%	

Detailed Reports			
Errors	Warnings	Infos	
0	4 Warnings (4 new)	5 Infos (5 new)	

Secondary Reports	
Generated	
Date Generated: 08/27/2013 - 14:12:08	

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